

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: Mitsushi FUJIKI Group Art Unit: 2823

Application Number: 10/772,253 Examiner: Thanh V. Pham

Filed: February 6, 2004 Confirmation Number: 6491

For: METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE

INCLUDING A LOW-TEMPERATURE LOWER ELECTRODE-

FORMING STEP

Attorney Docket Number: 042068

Customer Number: 38834

RESPONSE UNDER 37 C.F.R. § 1.111

Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450 February 21, 2007

Sir:

In response to the Office Action dated November 22, 2006, Applicants submit the following remarks, which begin on page 2 of this paper.